

ABSTRACT

**RECEIVER HAVING A VARIABLE THRESHOLD SLICER STAGE
AND A METHOD OF UPDATING THE THRESHOLD LEVELS
OF THE SLICER STAGE**

A receiver having a variable bit slicer for detecting bits in a demodulated signal, comprises a demodulator (14) for deriving a demodulated bit rate signal, means (36) for storing a plurality of threshold values, each of the threshold values being selectively adjustable, means (28, 38) for selecting the threshold value for comparison with the current bit signal (S_n) in response to a sequence of N bits (where N is at least 2) (B_{n-1} , B_{n-2}) received prior to the current bit (B_n) and means (38, 40) for using the current bit to update the selected threshold value.

Also disclosed is a method of dc offset correction (Figure 7 - not shown).

(Figure 1)